

## **AMENDMENTS TO THE SPECIFICATION**

**Please replace the paragraph at page 13, line 4, with the following rewritten paragraph:**

The address conversion unit 21 converts the lower address of the second processor 20 for outputting to the shared memory 30. In relation to the above-mentioned~~above-mentioned~~ (B), this address conversion converts the lower address of data that is smaller than the 32-bit basic word length, to indicate a reversed position within the basic word length, for the concerned data.